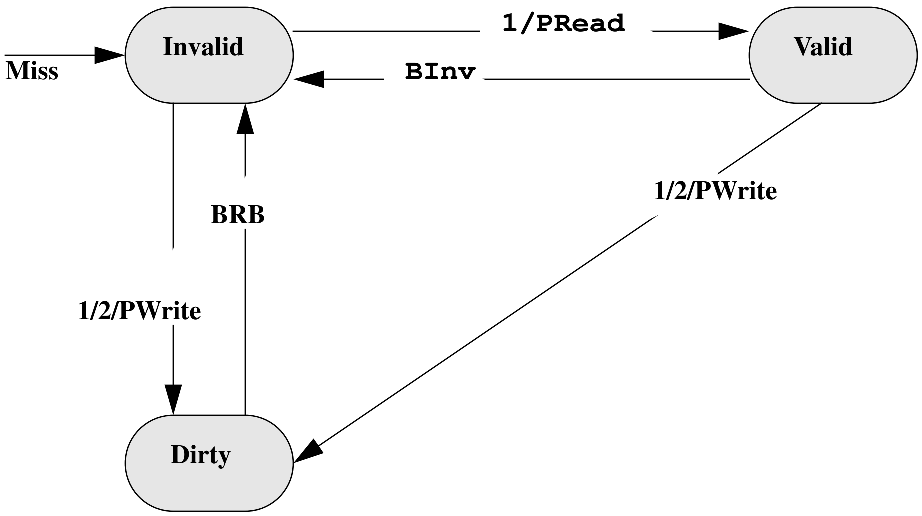
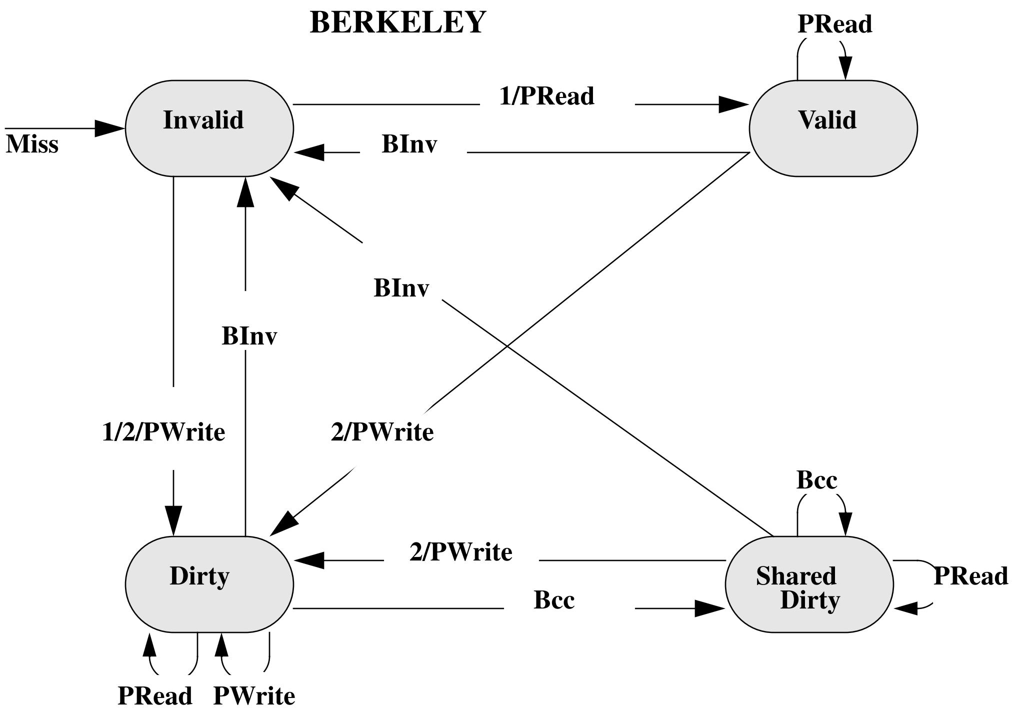
SYNAPSE



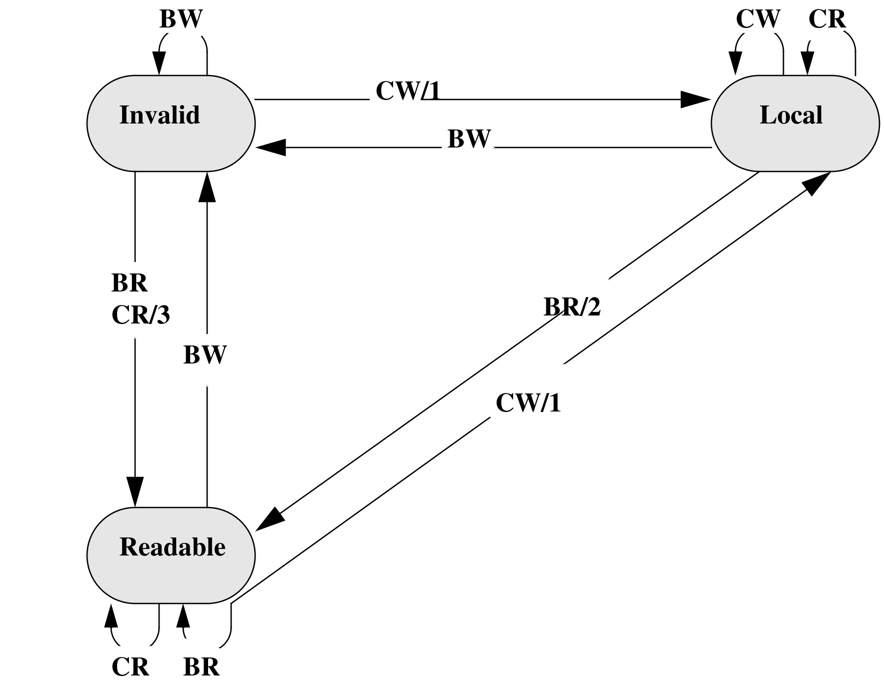

```
Bus Actions:1.a.Bus request to the owner
               to return data to memory (BRB)
              b. Read-in data from memory
           2.  Bus Invalidate (BInv)
```

BERKELEY

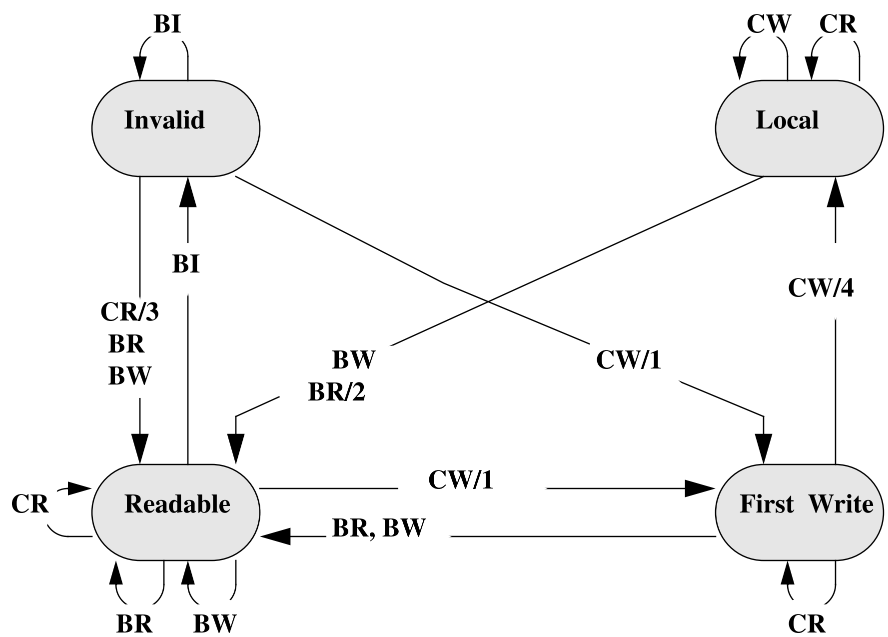



```
Bus Actions: 1. Bus request to the owner
                to read data into local
                cache (Bcc)
             2. Bus Invalidate (BInv)
```

READ BROADCAST



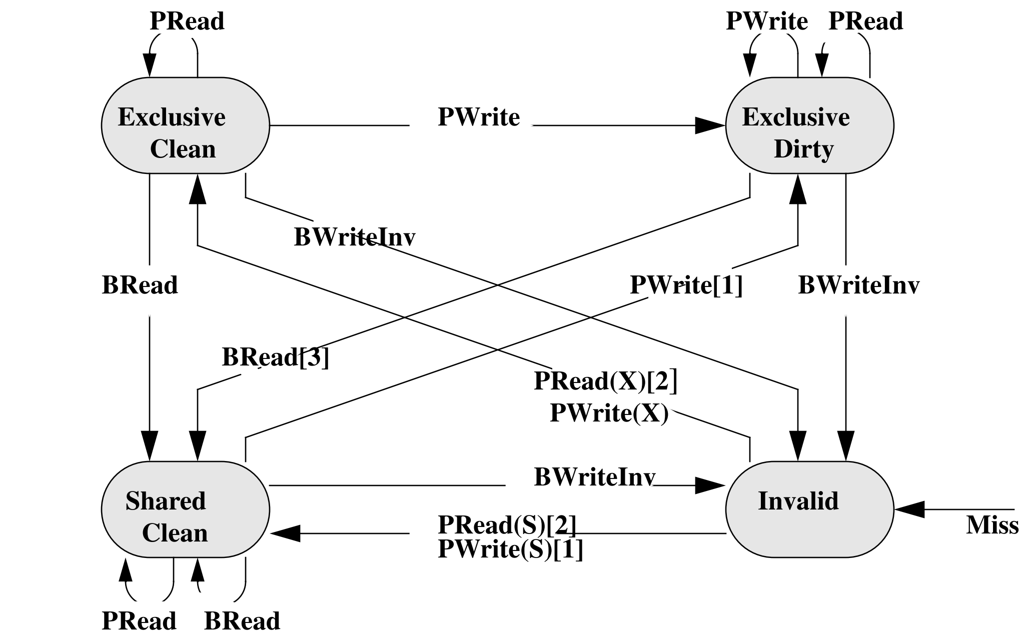
READ WRITE BROADCAST



Legend : CW -- CPU Write Request
CR -- CPU Read Request
BW -- Bus Write Request
BR -- Bus Read Request
BI -- Bus Invalidate Request

Modifiers : 1. generates a BW (write through)
2. interrupts BR and supply the data from the cache
3. generates a BR (cache miss)
4. generates a BI

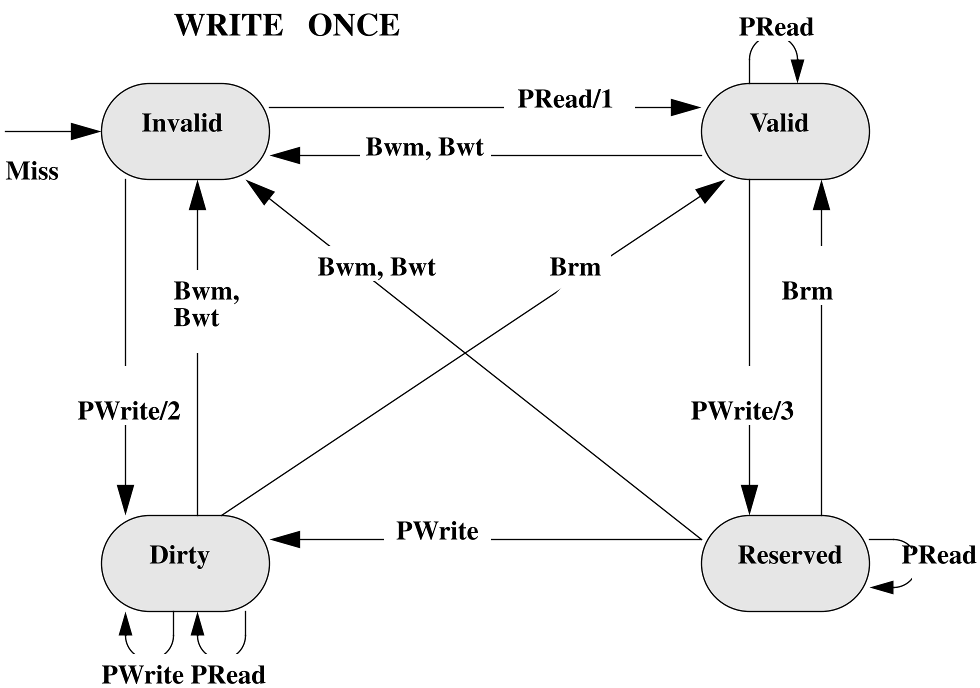
ILLIONOIS WRITE INVALIDATE



Bus Actions: [1] BWriteInv
[2] BRead
[3] Write Through

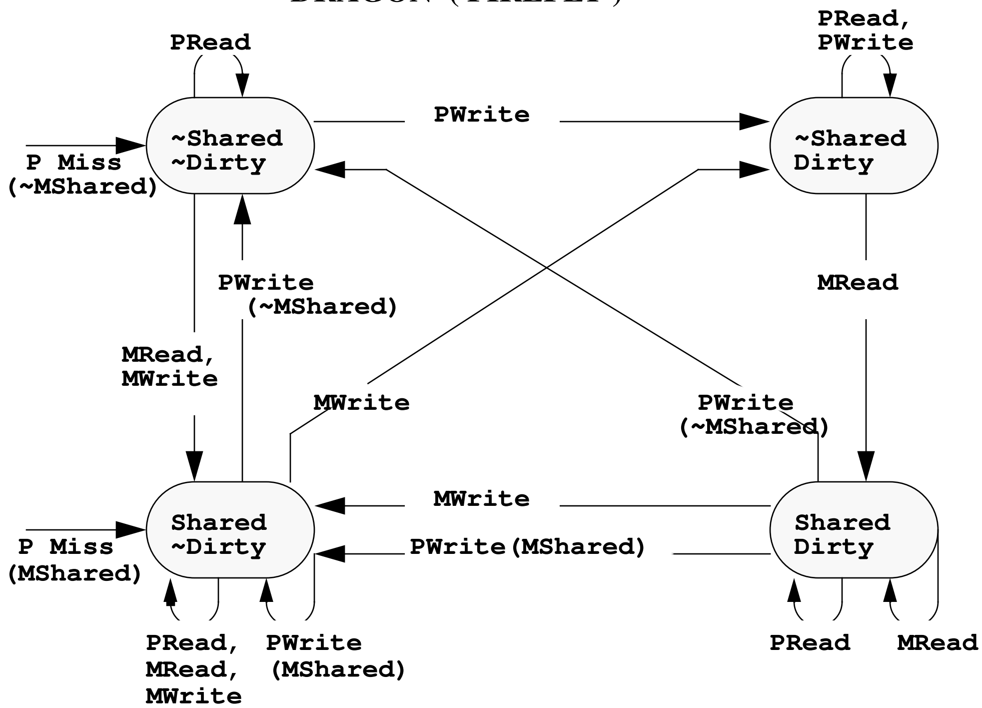
Conditions : X Exclusive
(bus arbitr.) S Shared

WRITE ONCE

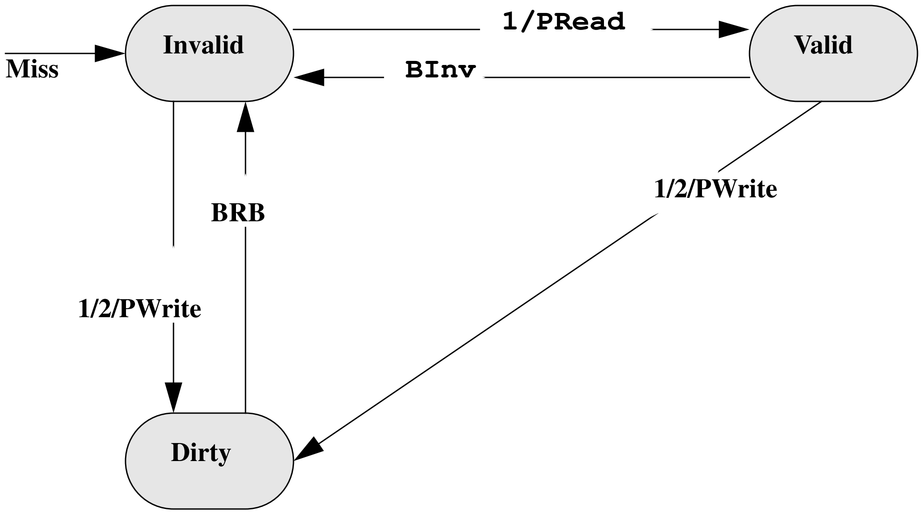


Bus Actions 1. Read miss request (Brm)
2. Write miss request (Bwm)
3. Write Trough (one word write - Bwt)

DRAGON (FIREFLY)



SYNAPSE

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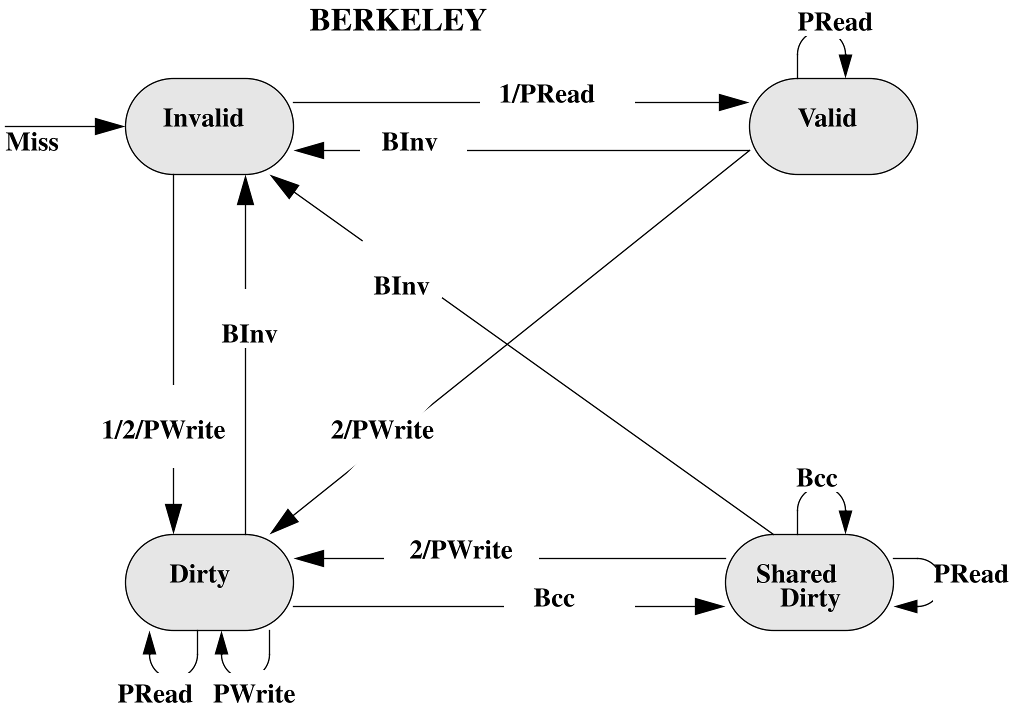
BERKELEY

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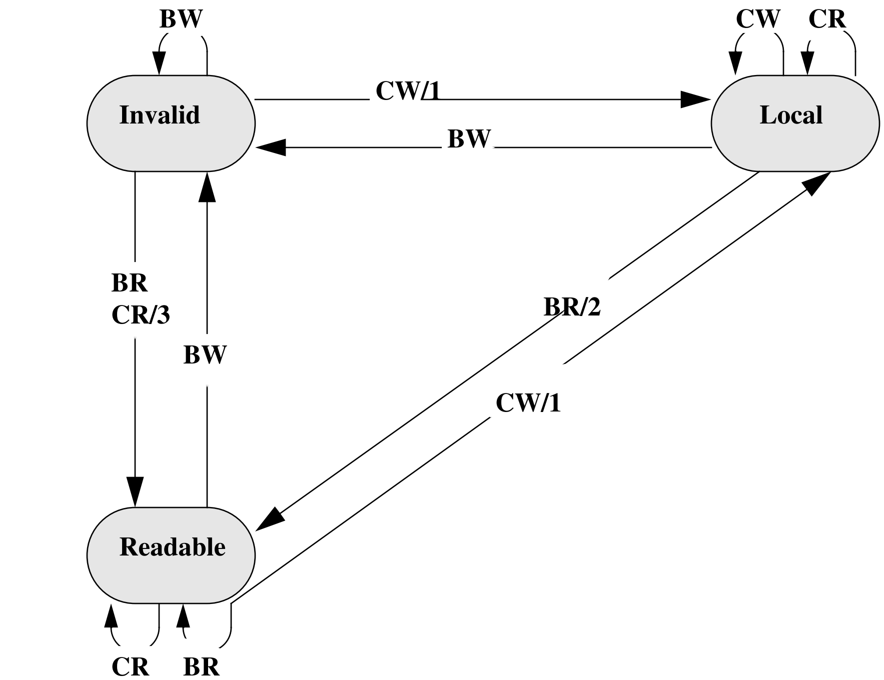
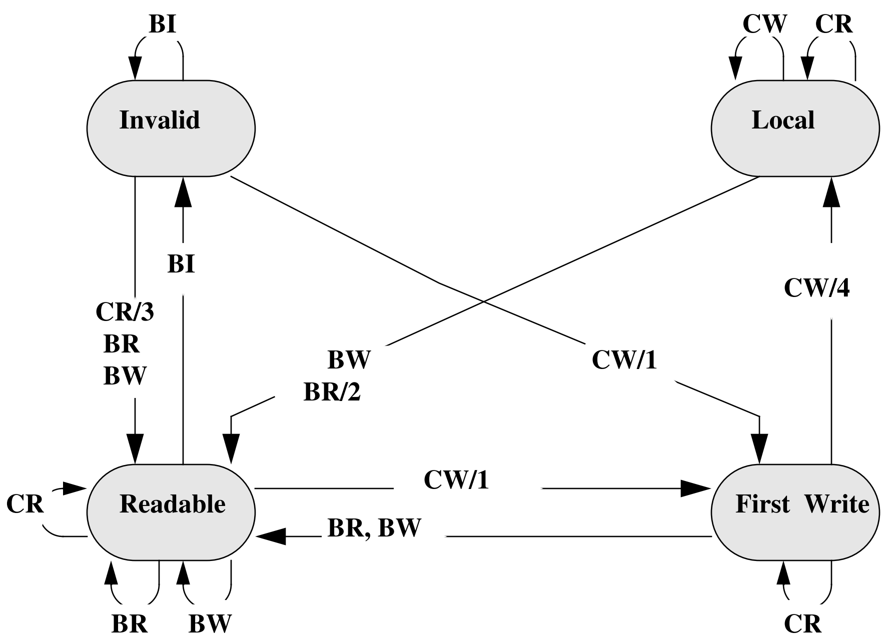
READ BROADCAST



READ WRITE BROADCAST



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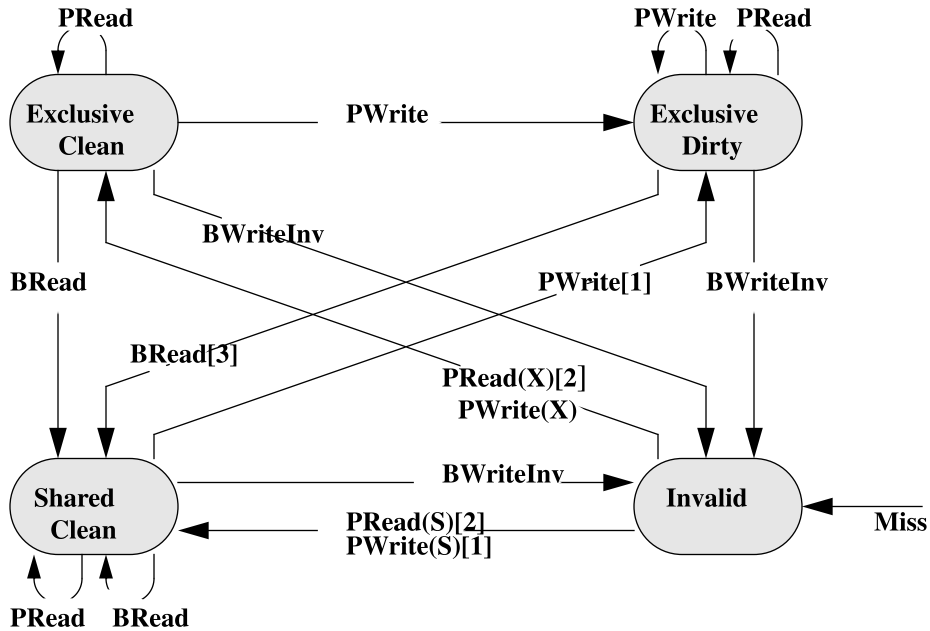
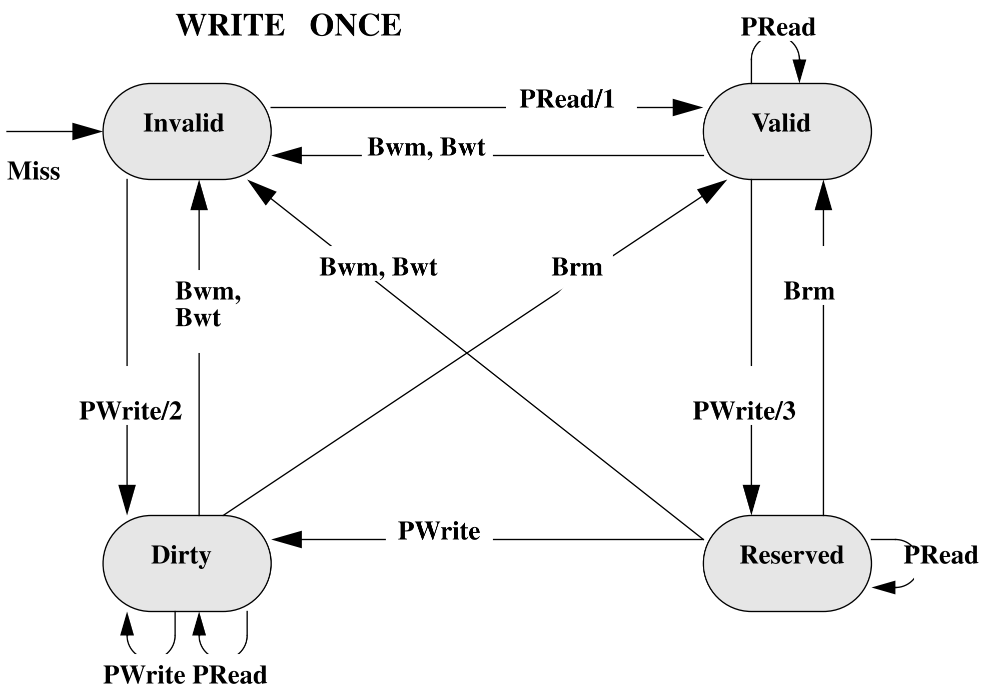
ILLIONOIS WRITE INVALIDATE



Bus Actions: [1] BWriteInv
[2] BRead
[3] Write Through

Conditions : X Exclusive
(bus arbitr.) S Shared

WRITE ONCE



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DRAGON (FIREFLY)