Problem Set Two Due on Tue 3/14/96	**Advanced Computer Architecture** CLA CS-550 / Spring 1996	Prof. Azer Bestavros Email: best@cs.bu.edu

Please do not underestimate the length of this problem set. Start as early as you can.
Your grade will depend on the clarity and soundness of your assumptions

1 General Pipelining Problems

Solve problems 3.3, 3.5, 3.9, 4.6, 4.7, 4.8, and 4.10, from Hennesy and Patterson, "Computer Architecture: A Quantitative Approach" 2nd Edition.

2 DLX Basic Pipeline Architecture

Check the pipelined implementation of the DLX architecture described in the report available on the web from `http://www.cs.bu.edu/faculty/best/crs/cs550/fridman/dlxsim.html`

Solve the two exercises included at the end of that report. The pipelined DLX simulator code is available under directory `/home/course/cs550/pdlx/`. Included in that directory as well are the files for the code used for exercises 1 and 2.

3 DLX Dynamic Pipeline Architecture

Check the dynamic pipelined implementation of the DLX architecture as described in the report available on the web from `http://www.cs.bu.edu/faculty/best/crs/cs550/yueh/dlxsim.html`

Solve the two exercises included at the end of the `Scoreboard` section and the `Tomasulo` section. The code for this simulator is available from `/home/course/cs550/ddlx/`.